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Electrochemical Fabrication of Cadmium Chalcogenide Microdiode Arrays

by

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13. ABSTRACT (Maximum 200 words) Arrays containing greater than 10^9 2000Å-diameter CdSe or graded CdSe/CdTe cylinders have been electrochemically synthesized within the pores of Anopore tm membranes. The alumina template suppresses the "cauliflower" morphology that is typically observed in electrodeposited CdSe and CdTe films, although X-ray diffraction reveals the materials to be of poor crystallinity. The Anopore membrane template can be removed by treatment with aqueous 1M NaOH, resulting in an array of free-standing CdSe or CdSe/CdTe wires. Current-voltage data show that the Ni/CdSe array is rectifying, with a rectification ratio of 1000 at ± 2 V.			
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Palmer^{*}, Charles R. Martin,^{***} and Michael J. Sailor^{***}

Arrays containing $>10^9$ 2000Å-diameter CdSe or
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Organic and metal microfibrils and microtubules have been synthesized using the pores of microporous membranes as geometric templates (1-6). The membranes employed have linear, cylindrical pores of uniform diameter, and so the microfibrils and microtubules obtained have highly monodisperse dimensions. These novel microstructures have potential applications in optical and electronic devices, sensors, and drug delivery systems (7, 8). In this work, we have developed the chemistry for fabricating the II-VI semiconductors CdSe and CdTe within template membranes of this type; arrays of semiconductor microcylinders are obtained (9). In contrast to the precipitation techniques developed to fabricate semiconductors within glass (11-15), polymer (16-18), or zeolitic matrices (19-22), the present technique employs electrodeposition, which provides a means to carefully control both the dimensions and composition of the semiconductor material. The results represent the first synthesis of very large ($> 10^9$) semiconductor diode arrays. In addition, the restricted dimensions of the membrane templates are found to eliminate the fractal-like cauliflower morphology often observed in electrodeposited II-VI materials.(23-26) These results may be important for the fabrication of high-resolution optical detectors, solar cells, or high-definition displays.

Anopore membranes obtained from Anotech Separations, Ltd. were used as templates for the electrodeposition of the II-VI semiconductors. The membranes are ca. 50 μm thick and contain tightly placed 2000 Å-diameter pores. A 300 Å layer of Au was sputter-coated onto one side of the Anopore membrane in order to form a conductive surface. A Ni substrate layer was then electrochemically plated (Nickel "S" sulfamate electroplating solution, Technic Inc., Providence, RI) on top of the Au. The Ni completely covered one side of the Anopore membrane, and partially filled the pores. The membrane was then separated into squares of approximately 1 cm^2 area and the Ni-coated side of each was attached to a wire using conductive Ag paint (Micro-Circuits

Company). The wire contacts and edges of the membranes were completely sealed in epoxy (Hysol 1C Epoxy Patch Kit) or with a Teflon gasket. Electrolyte solutions were prepared using 18 M Ω DI water and the following reagents: 3CdSO₄·8H₂O (98%), TeO₂ (99.995%) and SeO₂ (99.999%) (Aldrich Chemicals), ultrahigh purity H₂SO₄ (Baker Ultrex II, Baker Chemicals), and reagent grade HCl (Fisher Scientific).

Cadmium Selenide was then electrochemically deposited into the membrane using a previously developed ac electrodeposition technique.(27) The deposition bath typically contained 0.3M CdSO₄, 0.25M H₂SO₄, and 0.7 mM SeO₂ or TeO₂. If lower concentrations of chalcogen were used, elemental analysis (semiquantitative electron beam-induced X-ray fluorescence, EDS) revealed that the microcylinders contained excess Cd. At 0.7 mM concentrations of chalcogen, the electrodeposited materials showed near 1:1 stoichiometry by EDS, based on comparison with authentic bulk samples. All solutions were stirred during deposition. It was found that the electrodeposition tended to be more homogeneous when carried out in the absence of light.

Cross-sectional SEM images of the electrodeposited CdSe (Fig. 1) reveal that the cauliflower growth morphology, typical of electrodeposited CdSe and CdTe (27), is suppressed by the Anopore membrane. Thus template restriction of the growth dimensions can improve the overall morphology of these electrodeposited materials. As was observed in our work on electrodeposited thin films, the X-ray powder diffraction peaks are broad, and show the CdSe to be primarily the zincblende phase (27). Analysis of the width of the peaks using the Debye-Scherrer relationship (28, 29) indicates that the crystallite size in this material is on the order of 200-300 nm.

Current-voltage data on the Ni/CdSe structure (Fig. 2) was obtained by placing a tungsten carbide probe tip on the top of the CdSe layer after the Anopore layer had been removed (soaked in 1 M NaOH solution for 30 m, rinsed with 18 M Ω H₂O, dried at 100 °C for 1 h). Hot-probe measurement of CdSe thin films electrodeposited on a Ni plate (i.e. without using the Anopore template) showed that the

as-formed CdSe is n-type (typical for electrodeposited CdSe (30)), and point-contact current-voltage curves taken on these flat samples were similar to the curves of Fig. 2. The current-voltage data thus indicate that the CdSe/Ni interface is rectifying. The rectification characteristics of the CdSe cylinders are poor (relative to commercial Schottky junction diodes), with a forward to reverse bias rectification ratio of 1000 at ± 2 V. The current-voltage curves from many points were nonexponential, indicative of high series resistances in the devices. Current-voltage data from some points exhibited lower rectification ratios, presumably due to large shunting currents from direct contact between the probe tip and some of the underlying Ni substrate rods. The contacting method precludes determination of the exact area of the electronically active region, although using the surface area of the probe tip (radius 0.005 cm) as an upper estimate of the active area gives a minimum saturation current density (J_0) of 3×10^{-8} A/cm², or 10^{-17} A/cylinder

Thermal annealing would undoubtedly improve the electronic properties of these CdSe microcylinders (23, 31-34). However, we have found that annealing (400 °C) the materials results in loss of contact at the CdSe/Ni interface. We are currently exploring this problem further.

Graded CdSe/CdTe heterojunctions were prepared by initially depositing a predetermined amount of CdSe within the Anopore template, as described above. The electrodes were removed from the electrolyte solution, placed in the CdSO₄/TeO₂ solution, and an equal amount (as determined by coulometer) of CdTe was deposited. Scanning electron microscope elemental X-ray maps (Fig. 3) confirm the presence of Cd and Se in the lower section of the cylinder and Cd and Te in the top, while the transition region contains all three elements. The length of the transition region is on the order of the resolution of the mapping technique, and so a more quantitative analysis cannot be made.

Diffraction peaks for the CdTe layer were not observed in X-ray powder diffraction data on the CdSe/CdTe arrays. This is

consistent with previous CdTe electrodeposition results (24, 35), and indicates that the as-deposited CdTe is noncrystalline. Current-voltage data obtained from the heterojunctions were comparable to the Ni/CdSe junctions.

Template-restricted growth provides an improved morphology of electrodeposited semiconductors, although it does not yield enhanced electronic properties. It is interesting to point out that electron micrograph images of the billions of parallel, thin semiconductor cylinders in this work appear remarkably similar to retinal rod cells, the photoreceptors in the human eye.(36) The semiconductor rods are about a factor of 5 smaller in diameter than retinal rod cells. For these semiconductor "rods" to detect light analogous to the way the retina "sees," they will have to be made independently addressable, and will have to convert light into electric signals with reasonable efficiency. Both of these properties have yet to be demonstrated for the semiconductor microcolumn arrays.

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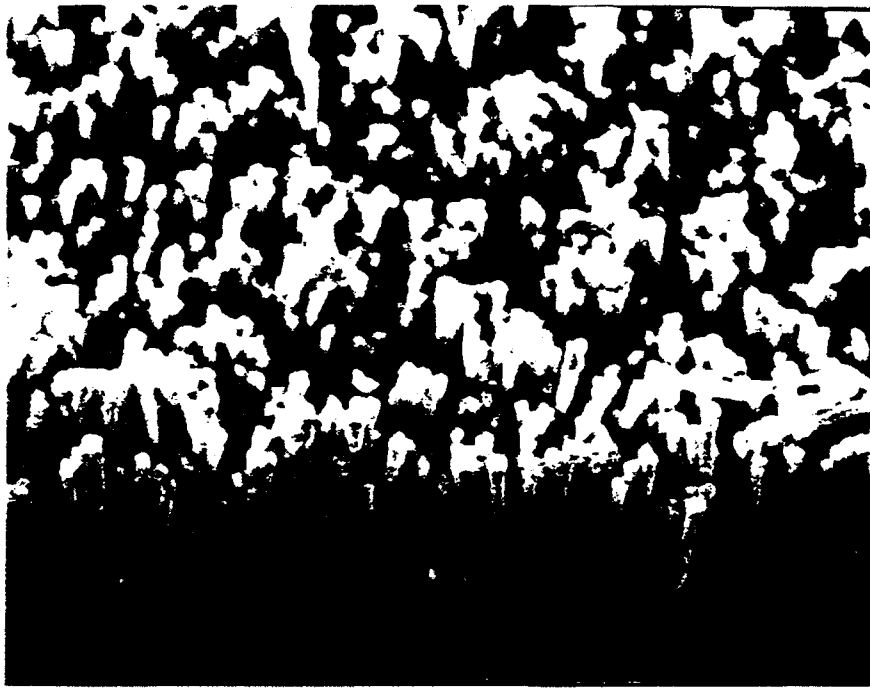
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Figure Captions

Figure 1. Scanning electron microscope image (20 keV, secondary electron image) of a CdSe diode array electrodeposited on a Ni substrate, using an Anopore membrane template. The Anopore membrane has been removed by soaking the electrode in 1M NaOH.

Figure 2. Forward (solid) and reverse-biased (dashed) current-voltage curves of a CdSe diode array. The contact area corresponds to between 10^5 and 10^6 individual CdSe rods.

Figure 3. Cross-sectional scanning electron microscope image and X-ray element maps of a CdSe/CdTe heterojunction array. (a) secondary electron image, 20 keV; (b) Ni map; (c) Cd map; (d) Se map; and (e) Te map. Bright regions in the element maps correspond to higher relative concentration of the element. A Cambridge Model 360 electron microscope with a LINK analytical AN10 X-ray energy analyzer attachment was used for these semiquantitative element maps. X-ray lines and energy windows (in keV) were as follows: Ni ($K\alpha$), 7.220-7.720; Cd ($L\alpha_1$), 2.940-3.320; Se ($K\alpha_1$), 10.940-11.520; Te ($L\alpha_1$), 3.560-3.960.



5 μm

